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Majid Pakdel



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Technology and Engineering Applications of Simulink S. Chakravarty, 2012-05-23 Building on MATLAB the language of technical computing Simulink provides a platform for engineers to plan model design simulate test and implement complex electromechanical dynamic control signal processing and communication systems Simulink Matlab combination is very useful for developing algorithms GUI assisted creation of block diagrams and realisation of interactive simulation based designs The eleven chapters of the book demonstrate the power and capabilities of Simulink to solve engineering problems with varied degree of complexity in the virtual environment

FPGA-based Digital Convolution for Wireless Applications Lei Guan, 2017-01-16 This book presents essential perspectives on digital convolutions in wireless communications systems and illustrates their corresponding efficient real time field programmable gate array FPGA implementations FPGAs or generic all programmable devices will soon become widespread serving as the brains of all types of real time smart signal processing systems like smart networks smart homes and smart cities The book examines digital convolution by bringing together the following main elements the fundamental theory behind the mathematical formulae together with corresponding physical phenomena virtualized algorithm simulation together with benchmark real time FPGA implementations and detailed state of the art case studies on wireless applications including popular linear convolution in digital front ends DFEs nonlinear convolution in digital pre distortion DPD enabled high efficiency wireless RF transceivers and fast linear convolution in massive multiple input multiple output MIMO systems After reading this book students and professionals will be able to Understand digital convolution with inside out information discover what convolution is why it is important and how it works Enhance their FPGA design skills i e enhance their FPGA related prototyping capability with model based hands on examples Rapidly expand their digital signal processing DSP blocks to examine how to rapidly and efficiently create DSP functional blocks on a programmable FPGA chip as a reusable intellectual property IP core Upgrade their expertise as both thinkers and doers minimize close the gap between mathematical equations and FPGA implementations for existing and emerging wireless applications

Advancing VLSI through Machine Learning Abhishek Narayan Tripathi, Jagana Bihari Padhy, Indrasen Singh, Shubham Tayal, Ghanshyam Singh, 2025-03-31 This book explores the synergy between very large scale integration VLSI and machine learning ML and its applications across various domains It investigates how ML techniques can enhance the design and testing of VLSI circuits improve power efficiency optimize layouts and enable novel architectures This book bridges the gap between VLSI and ML showcasing the potential of this integration in creating innovative electronic systems advancing computing capabilities and paving the way for a new era of intelligent devices and technologies Additionally it covers how VLSI technologies can accelerate ML algorithms enabling more efficient and powerful data processing and inference engines It explores both hardware and software aspects covering topics like hardware accelerators custom hardware for specific ML tasks and ML driven optimization techniques for chip

design and testing This book will be helpful for academicians researchers postgraduate students and those working in ML driven VLSI

Fundamentals of VHDL for FPGA Programming Using Vivado Majid Pakdel,2025-10-14 Enables readers to understand VHDL in the context of FPGA programming with a focus on the Vivado Design Suite Fundamentals of VHDL for FPGA Programming Using Vivado is a comprehensive guide designed to introduce readers to VHSIC Hardware Description Language VHDL and its application in Field Programmable Gate Array FPGA programming particularly using the Vivado Design Suite by Xilinx The inclusion of hands on protocol based projects for FPGA and MicroBlaze allows readers to apply what they have learned in practical scenarios helping to reinforce understanding and develop problem solving skills This book includes information on What FPGAs are how they work and why they are widely used in digital systems due to various advantages Basic concepts of VHDL necessary for understanding digital design including syntax data types and structures Best practices in VHDL coding and FPGA design to enhance the quality of designs and reduce debugging time The Vivado toolchain and its use in designing simulating and implementing VHDL code on FPGA devices Accessible yet comprehensive Fundamentals of VHDL for FPGA Programming Using Vivado is an essential learning resource for students aiming to start their careers in FPGA or VLSI system design and new professionals in the FPGA field seeking to build foundational skills and knowledge

Control System Analysis & Design in MATLAB and SIMULINK Mohammad Nuruzzaman,2014-06-20 Control System Analysis Design in MATLAB and SIMULINK is blueprinted to solve undergraduate control system engineering problems in MATLAB platform Unified view of control system fundamentals is taken into account in the text One key aspect of the text is the presentation of computing and graphing materials in a simple intuitive way Many advances in virtual implementation on control systems have been seen in the past decade The text elucidates the web of concepts underpinning these advances Self working out illustrations and end of chapter exercises enthuse the reader a checkup on thorough understanding The comprehensive introduction will benefit both undergraduates and graduates studying control system and engineering Also researchers in the field can have the text as reference

An Introductory Guide to EC Competition Law and Practice Valentine Korah,1994

The ... IEEE Asia Pacific Conference on ASICs ,1999

Embedded MATLAB-Real-Time Deployment on ARM, FPGA, and GPU Rhys Gallagher,2026-01-20 Is your groundbreaking control algorithm or signal processing model trapped inside a simulation waiting to be unleashed onto real world hardware with precision and speed Embedded MATLAB serves as the definitive bridge between abstract design and tangible deployment 1 This hands on guide dismantles the barriers of manual coding empowering you to translate sophisticated Simulink models directly into optimized C C HDL or CUDA code for the industry s most versatile platforms Designed for engineers researchers and developers who refuse to compromise on performance this book transforms you from a model designer into a full stack embedded deployment expert You will navigate the complex ecosystem of code generation and hardware integration with confidence What you will master Accelerate Development Bypass error prone manual coding by mastering Simulink Coder

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practical snapshots to illustrate the procedure of the subject. It is hoped that this manual book will be an immense use to Verilog learners and programmers. Writing the Verilog code for the digital circuits and simulate using any HDL simulator synthesis software Xilinx Modelsim Simulink etc and download to FPGA CPLD trainerkits

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Evaluating Simulink HDL Coder as a Framework for Flexible and Modular Hardware Description Valerie Youngmi Sarge, 2018 This thesis investigates the performance and viability of Simulink and HDL Coder from MathWorks as an alternative workflow for producing hardware description. Several designs were implemented towards this end. An FFT based signal analyzer served as a pathfinding application to better understand the tools. In order to directly evaluate the ability of the workflow to faithfully recreate hardware operations an existing architecture for nonlinear equalization was re implemented and benchmarked. Finally a new implementation of polynomial nonlinear equalization was created and benchmarked to explore the possible performance parameterizability and flexibility of hardware generated from a Simulink design. It was found that while the generated hardware does not perform quite as well as a hand optimized design it does perform well enough to be practical and also can be capable of greater flexibility in structure than a design created with a more traditional workflow.

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