



# Verilog Manual Assertion

```

always_comb
  if (sel == 1'b1)
    out = a;
  else
    //pragma translate off
    if (sel == 1'b0)
      //pragma translate on
      out = b;
    //pragma translate off
    else // sel == 1'bX or 1'bZ
      begin
        out = 'x;
        $display(%m "assert:
          sel X or Z at time %d",
          $time);
      end
    //pragma translate on
  
```



```

always_comb
  if (sel) out = a;
  else out = b;
  
```



# Microprocessor And Verilog And Manual

**Jianjun Gao**



## **Microprocessor And Verilog And Manual:**

## Reviewing **Microprocessor And Verilog And Manual**: Unlocking the Spellbinding Force of Linguistics

In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is really astonishing. Within the pages of "**Microprocessor And Verilog And Manual**," an enthralling opus penned by a highly acclaimed wordsmith, readers embark on an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve to the book is central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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