LPC2148 PWM Programming

In this tutorial, we are going to discuss the PWM module of LPC2148. First, we will see how to configure the PWM registers to generate signals of required PWM, at the end, we will see how to use the Explore Embedded PWM library.

LPC2148 PWM Module

LPC2148 has 6 PWM output pins which can be used as 6-Single edged or 3-Double edged. There as seven-match registers to support these 6-PWM output signals. Below block diagram shows the PWM pins and the associated Match (Duty Cycle) registers.

PWM Channel	Port Pin	Pin Functions	Associated PINSEL Register	Corresponding Match Register
PWM_1	P0.0	0-GPIO, 1-TXD0, 2-**PWM1**,	0,1 bits of PINSEL0	PWMMR1
PWM_2	P0.7	0-GPIO, 1-SSEL0, 2-**PWM2**, 3-EINT2	14,15 bits of PINSEL0	PWMMR2
PWM_3	P0.1	0-GPIO, 1-RXD0, 2-**PWM3**, 3-EINT0	2,3 bits of PINSEL0	PWMMR3
PWM_4	P0.8	0-GPIO, 1-TXD1, 2-**PWM4**,	16,17 bits of PINSEL0	PWMMR4
PWM_5	P0.21	0-GPIO, 1-**PWM5**, 2-CAP1.3,	10,11 bits of PINSEL1	PWMMR5
PWM_6	P0.9	0-GPIO, 1-RXD1, 2-**PWM6**, 3-EINT3	18,19 bits of PINSEL0	PWMMR6

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C. J. Silverio,Silicon Graphics, Inc

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