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Fast Hopping Frequency Generation in Digital CMOS

Fast Hopping Frequency Generation In Digital Cmos

Jouko Vankka, Kari A.I. Halonen



Fast Hopping Frequency Generation In Digital Cmos:

Fast Hopping Frequency Generation in Digital CMOS Springer,2012-10-13 [Fast Hopping Frequency Generation in Digital CMOS](#) Mohammad Farazian, Lawrence E. Larson, Prasad S. Gudem,2012-10-12 Overcoming the agility limitations of conventional frequency synthesizers in multi band OFDM ultra wideband is a key research goal in digital technology This volume outlines a frequency plan that can generate all the required frequencies from a single fixed frequency able to implement center frequencies with no more than two levels of SSB mixing It recognizes the need for future synthesizers to bypass on chip inductors and operate at low voltages to enable the increased integration and efficiency of networked appliances The author examines in depth the architecture of the dividers that generate the necessary frequencies from a single base frequency and are capable of establishing a fractional division ratio Presenting the first CMOS inductorless single PLL 14 band frequency synthesizer for MB OFDMUWB makes this volume a key addition to the literature and with the synthesizer capable of arbitrary band hopping in less than two nanoseconds it operates well within the desired range on a 1.2 volt power supply The author's close analysis of the operation stability and phase noise of injection locked regenerative frequency dividers will provide researchers and technicians with much food for developmental thought

Fast Hopping High-frequency Carrier Generation in Digital CMOS Technology Mohammad Farazian,2009 One of the challenges in implementing a frequency synthesizer for Multi band OFDM Ultra Wideband MB OFDM UWB is overcoming the agility limitations of conventional synthesizers The MB OFDM proposal for UWB divides the spectrum from 3.1 GHz to 10.6 GHz into 14 different bands and frequency hops at the rate of 3.2 MHz between them with a specified frequency settling time of only 9.5 nS Design techniques that eliminate the use of on chip inductors and which are compatible with low voltage operation are critical for increasing the level of integration for future implementations An inductor less design methodology may have several advantages over traditional design techniques 1 While the area required to implement an on chip inductor does not scale down in the finer technology nodes inductor less designs benefit from technology scaling 2 On the other hand the quality factor of the on chip inductors may worsen in finer technology nodes which can lead to an increase in the required current consumption to generate a given voltage swing 3 It is more straightforward to port an inductor less design into a new technology node The penalty for an inductor less design methodology is a slightly increase in the current consumption to achieve the necessary gain and voltage swing in the absence of inductors In this work a frequency plan is proposed that can generate all the required frequencies from a single fixed frequency and can implement any center frequency with a maximum of two levels of SSB mixing In order to generate all the required frequencies for the operation of this frequency synthesizer out of a single frequency fractional frequency dividers are needed Therefore a study is performed on the architectures that can obtain a fractional division ratio This study involves an analysis of the operation stability and phase noise of injection locked regenerative frequency dividers In addition the operation stability locking range and phase

noise of two stage ring oscillators which are compact ways to generate quadrature output phases and can be used in injection locked regenerative frequency dividers are analyzed This work presents the first CMOS inductor less single PLL 14 band frequency synthesizer for MB OFDM UWB which is capable to perform any arbitrary band switching specified in less than 2 nS Implemented in a 0.13 μm CMOS process it uses a single 1.2 V supply voltage and dissipates 135 mW The mixing sideband level is better than 31 dBc and the phase noise is better than 110 dBc/Hz at 1 MHz offset

Quadrature Frequency Generation for Wideband Wireless Applications Mohammad Elbadry, Ramesh Harjani, 2015-03-10 This book describes design techniques for wideband quadrature LO generation for software defined radio transceivers with frequencies spanning 4GHz to around 80GHz The authors discuss several techniques that can be used to reduce the cost and or power consumption of one of the key component of the RF front end the quadrature local oscillator The discussion includes simple and useful insights into quadrature VCOs along with numerous examples of practical techniques

Direct Digital Synthesizers Jouko Vankka, Kari A.I. Halonen, 2013-04-17 A major advantage of a direct digital synthesizer DDS is that its output frequency phase and amplitude can be precisely and rapidly manipulated under digital processor control Other inherent DDS attributes include the ability to tune with extremely fine frequency and phase resolution and to rapidly hop between frequencies These combined characteristics have made the technology popular in military radar and communications systems In fact DDS technology was previously applied almost exclusively to high end and military applications it was costly power hungry difficult to implement and required a discrete high speed D/A converter Due to improved integrated circuit IC technologies they now present a viable alternative to analog based phase locked loop PLL technology for generating agile analog output frequency in consumer synthesizer applications It is easy to include different modulation capabilities in the DDS by using digital signal processing DSP methods because the signal is in digital form By programming the DDS adaptive channel bandwidths modulation formats frequency hopping and data rates are easily achieved The flexibility of the DDS makes it ideal for signal generator for software radio The digital circuits used to implement signal processing functions do not suffer the effects of thermal drift aging and component variations associated with their analog counterparts The implementation of digital functional blocks makes it possible to achieve a high degree of system integration Recent advances in IC fabrication technology particularly CMOS coupled with advanced DSP algorithms and architectures are providing possible single chip DDS solutions to complex communication and signal processing subsystems as modulators demodulators local oscillators programmable clock generators and chirp generators The DDS addresses a variety of applications including cable modems measurement equipments arbitrary waveform generators cellular base stations and wireless local loop base stations

Direct Digital Synthesizers was written to find possible applications for radio communication systems It will have appeal for wireless and wireline communication engineers teachers and students

Low Power Circuits for Emerging Applications in Communications, Computing, and Sensing Fei

Yuan,2018-12-07 The book addresses the need to investigate new approaches to lower energy requirement in multiple application areas and serves as a guide into emerging circuit technologies It explores revolutionary device concepts sensors and associated circuits and architectures that will greatly extend the practical engineering limits of energy efficient computation The book responds to the need to develop disruptive new system architectures circuit microarchitectures and attendant device and interconnect technology aimed at achieving the highest level of computational energy efficiency for general purpose computing systems Features Discusses unique technologies and material only available in specialized journal and conferences Covers emerging applications areas such as ultra low power communications emerging bio electronics and operation in extreme environments Explores broad circuit operation ex analog RF memory and digital circuits Contains practical applications in the engineering field as well as graduate studies Written by international experts from both academia and industry

CMOS Single Chip Fast Frequency Hopping Synthesizers for Wireless Multi-Gigahertz Applications Taoufik Bourdi,Izzet Kale,2007-03-06 In this book the authors outline detailed design methodology for fast

frequency hopping synthesizers for RF and wireless communications applications There is great emphasis on fractional N delta sigma based phase locked loops from specifications system analysis and architecture planning to circuit design and silicon implementation The developed techniques in the book can help in designing very low noise high speed fractional N frequency synthesizers

Wireless Technologies Krzysztof Iniewski,2017-12-19 Advanced concepts for wireless technologies present a vision of technology that is embedded in our surroundings and practically invisible From established radio techniques like GSM 802 11 or Bluetooth to more emerging technologies such as Ultra Wide Band and smart dust motes a common denominator for future progress is the underlying integrated circuit technology Wireless Technologies responds to the explosive growth of standard cellular radios and radically different wireless applications by presenting new architectural and circuit solutions engineers can use to solve modern design problems This reference addresses state of the art CMOS design in the context of emerging wireless applications including 3G 4G cellular telephony wireless sensor networks and wireless medical application Written by top international experts specializing in both the IC industry and academia this carefully edited work uncovers new design opportunities in body area networks medical implants satellite communications automobile radar detection and wearable electronics The book is divided into three sections wireless system perspectives chip architecture and implementation issues and devices and technologies used to fabricate wireless integrated circuits Contributors address key issues in the development of future silicon based systems such as scale of integration ultra low power dissipation and the integration of heterogeneous circuit design style and processes onto one substrate Wireless sensor network systems are now being applied in critical applications in commerce healthcare and security This reference which contains 25 practical and scientifically rigorous articles provides the knowledge communications engineers need to design innovative methodologies at the circuit and system level

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web mentioned above 1 so we selected motorized hydraulic jack operation of motorised hydraulic jack is described it consists of a dc motor the electric

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web in this project we are converting the conventional hydraulic jack in to automated hydraulic jack by using linkage mechanism with a help of a motor such that the vehicles can be

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