
B.Sc.IT: Semester – II [Microprocessor & Microcontroller]
Question Paper (Sep – 2013) [Revised Course]

- **Karnel T.**

Time: 3 Hours

Total Marks: 100

N.B.: (1) All Questions are Compulsory.

- Q.1 Attempt Both The Questions: (10 Marks)**
- (A) Give classification of memory. (5)
(B) What is latch? Describe its function. (5)
- Q.2 Attempt Any Three From The Following: (15 Marks)**
- (A) Distinguish between SRAM and DRAM. (5)
(B) Explain encoder with suitable example. (5)
(C) Explain Tri-State Buffer in detail. (5)
(D) Explain related to memory: – (5)
(i) Expanding Memory (ii) Expanding Word Size
- Q.3 Attempt Any Three From The Following: (15 Marks)**
- (A) State functions of ALU of 8085. (5)
(B) What is flag? List and describe the flags of 8085. (5)
(C) Write short note on 8085 Address, Data and Control Bus. (5)
(D) State and describe the Interrupt Signal available in 8085. (5)
- Q.4 Attempt Any Three From The Following: (15 Marks)**
- (A) Classify the instructions on the basis of byte length with example. (5)
(B) Write an assembly language program to find the smallest number from memory block starting from 2000 H to 2005 H and store largest number in 2020 H. (5)
(C) Explain following instructions in 8085: – (5)
(i) ADI Data 8 (ii) STAX rp (iii) ANA r (iv) J-condition Address (v) RAR
(D) Explain the Programming Model of 8085. (5)
- Q.5 Attempt Any Three From The Following: (15 Marks)**
- (A) What are the benefits of RAID? (5)
(B) What is Cache Memory? Why is it needed? (5)
(C) What is fetch? Execute and instruction Cycle? (5)
(D) Explain the features of PCI Bus. (5)

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CH Cherryholmes



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