

SYMBOLS			SYMBOLS OF LA RES			SYMBOLS OF RESISTORS				SYMBOL OF INDUCTOR	SYMBOLS OF CAPACITORS			
RES	RES	RES	RES	RES	RES	RES	RES	RES	RES	IND	CAP	CAP	CAP	CAP

SYMBOLS OF METERS						SYMBOLS OF TRANSFORMERS					
AMP	AMP	AMP	AMP	AMP	AMP	TRF	TRF	TRF	TRF	TRF	TRF

SYMBOLS OF RELAYS			SYMBOLS OF SWITCHES											
REL	REL	REL	SW	SW	SW	SW	SW	SW	SW	SW	SW	SW	SW	SW

SYMBOLS OF DIODES			SYMBOLS OF TUBES											
DI	DI	DI	TUB	TUB	TUB	TUB	TUB	TUB	TUB	TUB	TUB	TUB	TUB	TUB

SYMBOLS OF LOGIC				SYMBOLS OF LOGIC GATES										
LOG	LOG	LOG	LOG	AND	OR	NOT	NAND	NOR	XOR	XNOR	AND	OR	NOT	NAND

SYMBOLS OF LOGIC				SYMBOLS OF LOGIC GATES										
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SYMBOLS OF LOGIC				SYMBOLS OF LOGIC GATES										
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# Iec Schematic Symbols For Autocad Electrical

**Peipei Pang**



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Center for Technical Knowledge,2016-03-21 The AutoCAD r Electrical 2017 R1 Fundamentals with IEC Standards student guide covers the indispensable core topics for working with the AutoCAD r Electrical software In this student guide students learn how to use many of the powerful electrical drawing creation tools in the AutoCAD Electrical software Students create schematic drawings ladder logic and point to point panel drawings and PLC I O circuits using automated commands for symbol insertion component tagging wire numbering and drawing modification In addition students are introduced to methods of customizing AutoCAD Electrical symbols circuits and databases Other topics covered include titleblock linking reporting tools templates and project files Topics Covered Understanding project files Creating and editing schematic and panel drawings Working with PLC symbols Creating custom symbols Generating reports Prerequisites Before taking this course students need to have a good working knowledge of the AutoCAD r software and electrical terminology *Electrical and Electronics Graphic Symbols and Reference Designations* Institute of Electrical and Electronics Engineers,American National Standards Institute,1976 *Letter Symbols to be Used in Electrical Technology* ,2008 **Logic Symbols and Diagrams** American National Standards Institute,Institute of Electrical and Electronics Engineers,1987 ANSI IEEE Std 91 1984 and ANSI IEEE Std 991 1986 provide methods by which engineers technicians and service people can describe and understand the behavior and implementation of a logic circuit

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