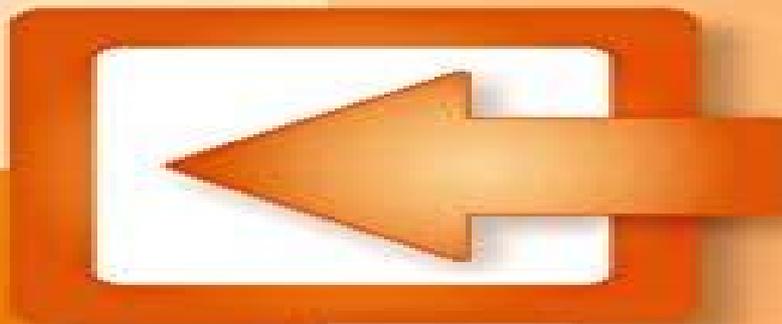


# Embedded System Design

**Modeling,  
Synthesis  
and Verification**



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# Embedded System Design Modeling Synthesis And Verification

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