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Tao Wei



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Polyimides Malay Ghosh,2018-02-06 Provides coverage on the full range of topics associated with polyimides including structure polymer fundamentals and product areas The text addresses both basic and applied aspects of the subject It details the synthesis of polyimides polyamideimides and fluorinated polyimides explains the molecular design of photosensitive polyimides and more

Through-Silicon Vias for 3D Integration John Lau,2012-09-20 A comprehensive guide to TSV and other enabling technologies for 3D integration Written by an expert with more than 30 years of experience in the electronics industry Through Silicon Vias for 3D Integration provides cutting edge information on TSV wafer thinning thin wafer handling microbumping and assembly and thermal management technologies Applications to highperformance high density low power consumption wide bandwidth and small form factor electronic products are discussed This book offers a timely summary of progress in all aspects of this fascinating field for professionals active in 3D integration research and development those who wish to master 3D integration problem solving methods and anyone in need of a low power wide bandwidth design and high yield manufacturing process for interconnect systems Coverage includes Nanotechnology and 3D integration for the semiconductor industry TSV etching dielectric barrier and seed layer deposition Cu plating CMP and Cu revealing TSVs mechanical thermal and electrical behaviors Thin wafer strength measurement Wafer thinning and thin wafer handling Microbumping assembly and reliability Microbump electromigration Transient liquid phase bonding C2C C2W and W2W 2 5D IC integration with interposers 3D IC integration with interposers Thermal management of 3D IC integration 3D IC packaging

The International Journal of Microcircuits and Electronic Packaging ,1998 **Adhesion in Microelectronics** K. L. Mittal,Tanweer

Ahsan,2014-08-25 This comprehensive book will provide both fundamental and applied aspects of adhesion pertaining to microelectronics in a single and easily accessible source Among the topics to be covered include Various theories or mechanisms of adhesion Surface physical or chemical characterization of materials as it pertains to adhesion Surface

cleaning as it pertains to adhesion Ways to improve adhesion Unraveling of interfacial interactions using an array of pertinent techniques Characterization of interfaces interphases Polymer polymer adhesion Metal polymer adhesion metallized polymers Polymer adhesion to various substrates Adhesion of thin films Adhesion of underfills Adhesion of molding compounds Adhesion of different dielectric materials Delamination and reliability issues in packaged devices Interface mechanics and crack propagation Adhesion measurement of thin films and coatings Proceedings ,1996 **Annual IEEE Semiconductor Thermal Measurement and Management Symposium** ,1998 **Cost Modeling for System Simulation** Puwei Huang,1994 Electronic Packaging and Production ,1993 *Illustrated Official Journal (patents)* Great Britain. Patent Office,1994 **Electronics Packaging Forum** James E. Morris,1990-12-15 *Conference Record* ,1992 IEICE Transactions on Communications, Electronics, Information, and Systems ,1991 Forthcoming Books Rose Arny,1994-04 Multichip Module Technology Handbook Philip E. Garrou,Iwona Turlik,1998 MCMs are electronic components that house multiple integrated circuits ICs upon a single chip Their use in design allow systems that are faster hotter and more reliable than those built with standalone ICs More and more the speed needs of electronic systems require MCMs This comprehensive handbook aims to provide designers with the knowledge needed to understand and work with MCMs Index to IEEE Publications Institute of Electrical and Electronics Engineers,1997 Issues for 1973 cover the entire IEEE technical literature IEEE/CPMT International Electronic Manufacturing Technology Symposium : [proceedings] ,1995 **Handbook Of Tape Automated Bonding** John H. Lau,1992 Handbook of tape automated bonding TAB is a one stop guide to the state of the art of TAB technology including TAB tape bump inner lead bonding encapsulation testing burn in outer lead bonding inspection rework thermal management and reliability For professionals active in TAB research and development those who wish to master TAB problem solving methods and those who must choose a high performance and cost effective packaging technique for their interconnect systems here s a timely summary of progress in all aspects of this fascinating field **Business Publication Advertising Source** ,2006

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