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Power supplies

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USB 2.0 interface, LPC4370 USB, reset, crystal, _fpga/led

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Boot mode, LPC4370 ICs, external spi flash

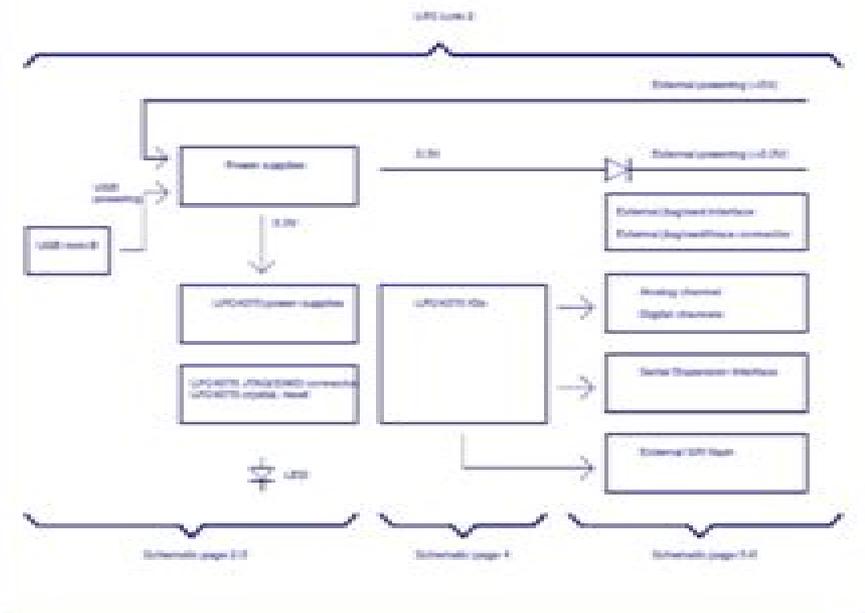
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External JTAG/Cortex debug interface

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Analog channel, digital channels, _fpga/trace interface

Design Overview



UL = UnLoaded = normally not mounted component.

Default jumper settings are indicated in the schematic. However, always check jumper positions on actual boards since there is no guarantee that all jumpers are in default place.

NXP Semiconductors

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Lpc Link 2 Schematic

Clifford Lane



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