

**Model Question Paper BASIC ELECTRONICS ENGINEERING (Assignment-III)**

**Time: 3 hrs.**

**MM: 100**

Note: Answer all the FIVE full questions.

Note: Answer all the Eight full questions as assignment-III

1) a. With appropriate circuit diagram explain the DC load line analysis of semiconductor diode. (5 Marks)

b. In a full wave rectifier, the input is from 30-0-30V transformer. The load and diode forward resistances are  $100\Omega$  and  $10\Omega$ , respectively. Calculate the DC average output voltage, dc output power, ac input power, rectification efficiency and percentage regulation. (5 Marks)

c. Explain the working of positive clamping circuit with diagrams & waveforms. (5 Marks)

d. In a Common Emitter transistor circuit if  $\beta = 100$  and  $I_B = 50\mu A$ , compute the values of  $\alpha$ ,  $I_E$  and  $I_C$ . (5 Marks) Answers:  $\alpha = 0.99$ ,  $I_E = 5.05 \text{ mA}$ ,  $I_C = 5 \text{ mA}$ .

Q.2) a. With a neat circuit diagram and waveforms explain the working of full wave bridge rectifier and show that its ripple factor is 0.48. (8 Marks)

b. Draw the common emitter circuit and sketch the input and output characteristics. Also explain active region, cutoff region, and saturation region by indicating them on the characteristic curve. Also, show the effect of base width modulation on the input and output characteristic curve of a Common Emitter BJT amplifier circuit. (6 Marks)

c. Design Zener voltage regulator for the following specifications: Input Voltage varies as  $10V \pm 20\%$ , Output Voltage = 5V,  $I_L = 20 \text{ mA}$ ,  $I_{Z_{min}} = 5 \text{ mA}$  and  $I_{Z_{max}} = 80 \text{ mA}$ . (6 Marks)

Hint:  $V_{in} \text{ (Range)} = (8V \text{ to } 12V)$ ,  $R_S = 120 \Omega$ ,  $R_L = 250 \Omega$ ,  $V_Z \text{ of Zener Z1} = 5V$ .

3) a. With a neat circuit diagram explain the Voltage Divider Bias circuit by giving its exact analysis. (8 Marks)

b. Determine the operating point for a silicon transistor biased by fixed bias method with  $\beta = 100$ ,  $R_B = 500 \text{ K}\Omega$ ,  $R_C = 2.5 \text{ K}\Omega$ ,  $R_E = 0 \Omega$ , and  $V_{CC} = 20V$ . Also draw the DC load line. (6 Marks) Answer: Q operating point coordinate: (10.35 V, 3.86 mA)

c. Derive the two-port h-parameter equations for common Emitter BJT amplifier. And define  $h_{ie}$ ,  $h_{oe}$ ,  $h_{fe}$  and  $h_{re}$  constants. (6 marks)

Q. 4) a. Derive the two-port h-parameter equations for common Base BJT amplifier. And define  $h_{ib}$ ,  $h_{ob}$ ,  $h_{fb}$  and  $h_{rb}$  constants. (7 marks)

Derive the two-port h-parameter equations for common collector BJT amplifier. And define  $h_{ic}$ ,  $h_{oc}$ ,  $h_{fc}$  and  $h_{rc}$  constants. (7 marks)

c. Sketch a CE BJT amplifier circuit and show that its voltage gain  $A_V = -\beta R_{AC} / R_{in}$ , where  $\beta = I_C / I_B$ , and ac equivalent load,  $R_{AC} = R_C || R_L = R_C \cdot R_L / (R_C + R_L)$ , and  $R_{in}$  = input impedance of the BJT input side part. (6 marks)

# G Sehem Paper For Ce Branch

**Thomas Griffiths**



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