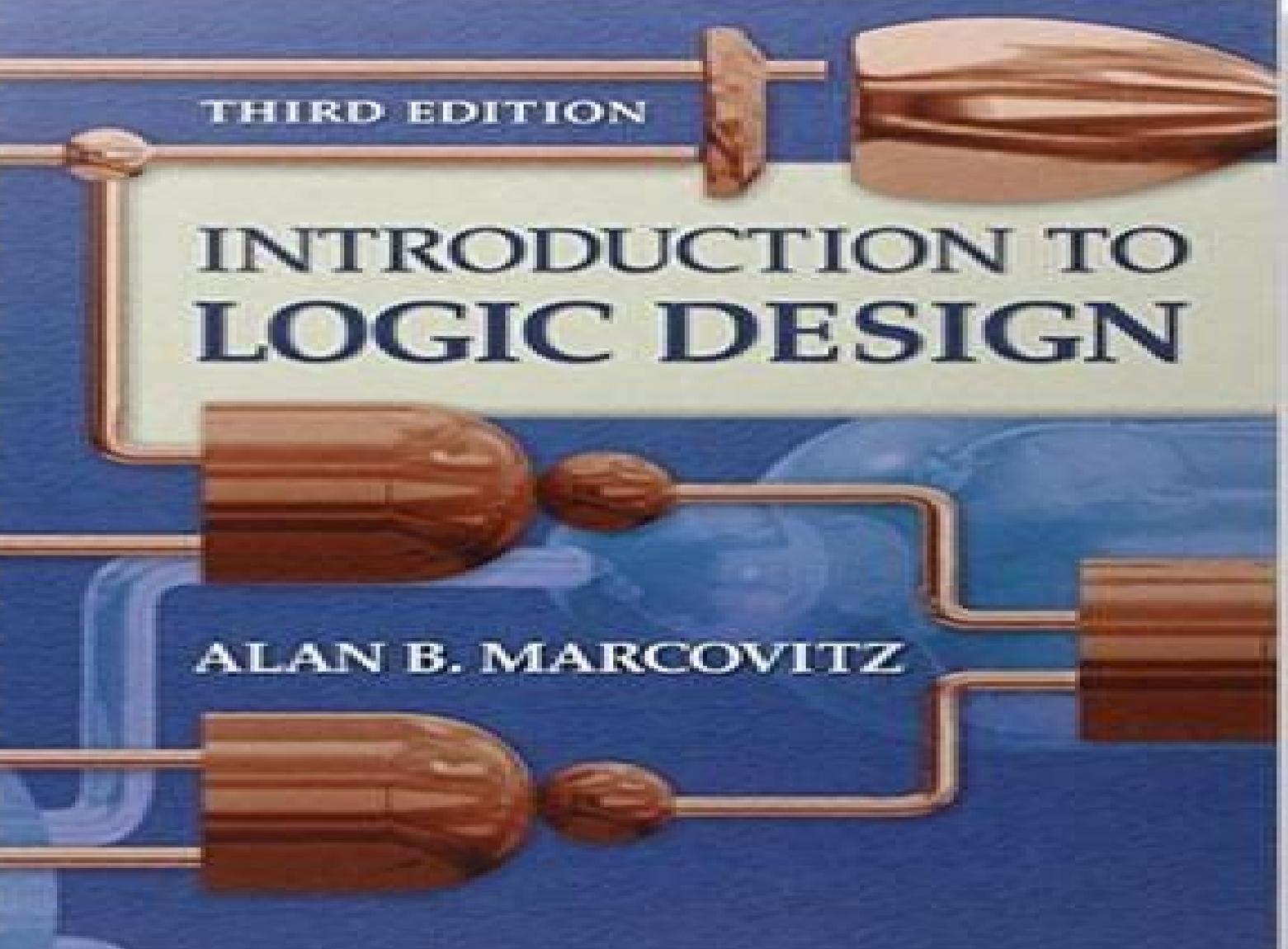


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THIRD EDITION

INTRODUCTION TO LOGIC DESIGN

ALAN B. MARCOVITZ



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The British National Bibliography Arthur James Wells, 2007 *Embedded SoPC Design with Nios II Processor and VHDL Examples* Pong P. Chu, 2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O

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