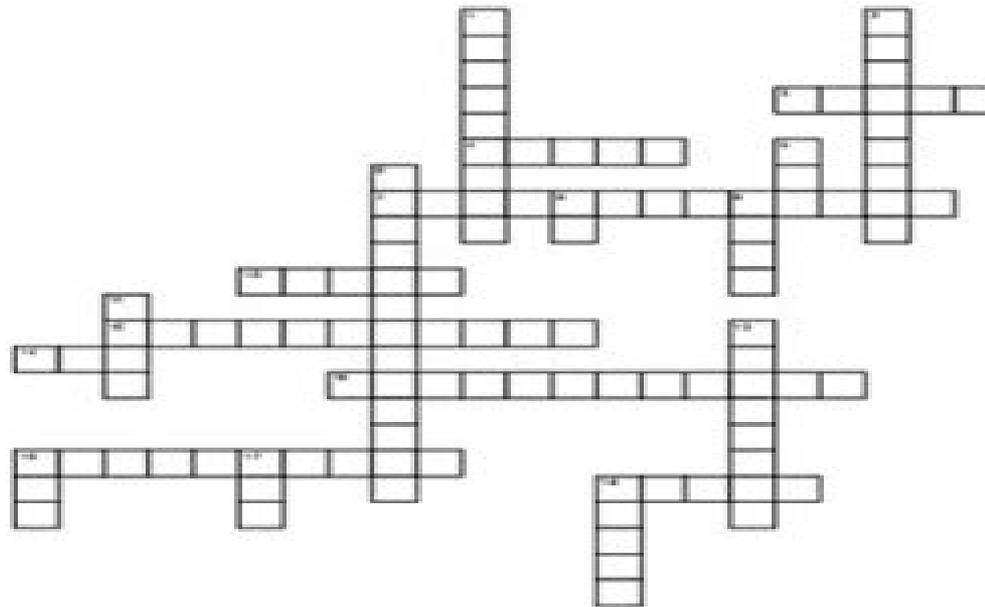


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Across

- [3] Timing signal is called as ____.
- [4] One input many output is called as ____.
- [7] A binary no. obtained by complementing every bit of a no.
- [10] AND, OR, and NOT are ____ gates.
- [12] $(A+B)+C = A+(B+C)$ is expression for ____ law.
- [14] Base of decimal number system
- [15] $A+BC = (A+B) \cdot (A+C)$ is expression for ____ law.
- [16] Circuit whose value depends on present input and previous output
- [18] Parity counter is application of ____ logic gate

Down

- [1] A combinational circuit performs addition of two numbers
- [2] Theorem used to change AND logic to OR and vice versa
- [5] Memory information is easily read and altered in ____ memory.
- [6] Memory is not required in ____ circuits.
- [8] 7432 is IC no of ____ logic gate
- [9] FlipFlops are ____ triggered circuit
- [11] Universal Gate
- [13] Basic building block of sequential circuit
- [16] Format of following Boolean expression is $Y = A + B + AB$?
- [17] If any one of input is 1 output is zero. This is ____ Gate
- [18] Number of distinct value in octal number systems

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Coats**



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