

Question Paper Code : 98081

M.E. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2010

Elective

VLSI Design

VL 9252 — LOW POWER VLSI DESIGN

(Common to M.E. Applied Electronics)

(Regulation 2009)

Time : Three hours

Maximum : 100 Marks

Answer ALL questions

PART A — (10 × 2 = 20 Marks)

1. Define subthreshold swing.
2. What is body effect?
3. What is meant by transistor reordering?
4. Define intrinsic delay.
5. Draw a 6 transistor SRAM cell.
6. Compare NOR type row decoder and NAND type row decoder in memories.
7. What is reconfigurable computing?
8. Implement function $AX^2 + BX + C$ using two multipliers and two adders.

Low Power Vlsi Design Question Paper

Peipei Pang



Low Power Vlsi Design Question Paper:

Basic Electronics Rakesh Kumar Garg, Ashish Dixit, Pavan Yadav, 2008 **UGC NET Paper 2 Electronics Science Chapterwise Question Bank | Solved Questions with Detailed Explanations | Based on Updated Exam Pattern** Nodia, 2026-03-09 Prepare effectively for the UGC NET Electronics Science examination with this comprehensive Chapterwise MCQ Bank designed to help aspirants strengthen their conceptual understanding and improve exam performance. The book includes a wide range of carefully selected multiple choice questions organized chapter wise allowing students to practice each topic systematically. Every question is accompanied by clear and detailed explanations enabling readers to understand the reasoning behind the correct answers and build a strong foundation in electronics concepts. This book strictly follows the latest UGC NET syllabus and updated exam pattern making it a reliable and practical preparation guide. The MCQs are designed to match the actual difficulty level and structure of the UGC NET exam helping candidates become familiar with the types of questions asked in the examination. With additional important and predicted questions for upcoming exams, this book serves as a valuable resource for revision practice and self assessment for all serious UGC NET Electronics Science aspirants. Proceedings of 6th International Conference on Recent Trends in Machine Learning, IoT, Smart Cities and Applications Vinit Kumar Gunjan, Jacek M. Zurada, 2026-01-01 This book includes original peer reviewed research articles from 6th International Conference on Recent Trends in Machine Learning IoT Smart Cities and Applications ICMISC 2025 held in March 28 29 2025 at CMR Institute of Technology Hyderabad India. It covers the latest research trends and developments in areas of machine learning smart cities IoT artificial intelligence cyber physical systems cybernetics data science neural network and cognition. *Digest of Technical Papers*, 1999 *Proceedings, 1997 International Symposium on Low Power Electronics and Design* IEEE Circuits and Systems Society, 1997 18th International Conference on VLSI Design International Conference on VLSI Design, 2005 **Scientific and Technical Aerospace Reports**, 1986-10 **Proceedings of ASP-DAC/VLSI Design 2002**, 2002 Papers from a January 2002 conference are organized into four sessions each on low power design synthesis testing layout and interconnects and technology as well as two sessions each on embedded systems verification and VLSI architecture one session on analog design and one session on hot c **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology**, 1996 **Energy-Efficient Distributed Computing Systems** Albert Y. Zomaya, Young Choon Lee, 2012-07-26 The energy consumption issue in distributed computing systems raises various monetary environmental and system performance concerns. Electricity consumption in the US doubled from 2000 to 2005. From a financial and environmental standpoint reducing the consumption of electricity is important yet these reforms must not lead to performance degradation of the computing systems. These contradicting constraints create a suite of complex problems that need to be resolved in order to lead to greener distributed computing systems. This book brings together a group of outstanding researchers that investigate the different facets of green and

energy efficient distributed computing Key features One of the first books of its kind Features latest research findings on emerging topics by well known scientists Valuable research for grad students postdocs and researchers Research will greatly feed into other technologies and application domains Eleventh International Conference on VLSI Design VLSI Society of India, IEEE Circuits and Systems Society, 1997 Areas covered in this work include physical design synthesis delay test and timing high level synthesis hardware software co design low power design verification VLSI synthesis testability enhancement asynchronous design diagnosis test and fault modelling and mixed signal design **Proceedings of the ... ACM Great Lakes Symposium on VLSI**, 2005 **Electrical & Electronics Abstracts**, 1997 **Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference**, 2002 **VLSI Architecture** Brian Randell, Philip C. Treleaven, 1983 *DATA COMMUNICATION AND COMPUTER NETWORKS* PAL, AJIT, 2013-11-02 Intended primarily as a textbook for the students of computer science and engineering electronics and communication engineering master of computer applications MCA and those offering IT courses the book provides a comprehensive coverage of the subject Basic elements of communication such as data signal and channel alongwith their characteristics such as bandwidth bit internal and bit rate have been explained Contents related to guided and unguided transmission media Bluetooth wireless technology developed for Personal Area Network PAN and issues related to routing covering popular routing algorithms namely RIP OSPF and BGP have been introduced in the book Various aspects of data link control alongwith their application in HDLC network and techniques such as encoding multiplexing and encryption decryption are presented in detail Characteristics and implementation of PSTN SONET ATM LAN PACKET RADIO network Cellular telephone network and Satellite network have also been explained Different aspects of IEEE 802.11 WLAN and congestion control protocols have also been discussed in the book Key Features Each chapter is divided into section and subsection to provide flexibility in curriculum design The text contains numerous solved examples and illustrations to bring clarity to the subject and enhance its understanding Review questions given at the end of each chapter are meant to enable the teacher to test student s grasping of the subject **Journal of Speech-language Pathology and Audiology**, 1993 *2002 IEEE 16th Annual Meeting Lasers and Electro-Optics Society* IEEE, IEEE Laser and Electro-Optics Society Staff, 2003-12 **Government Reports Announcements & Index**, 1986-03 **IEEE International Conference on Circuits and Computers, ICC 82**, 1982

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