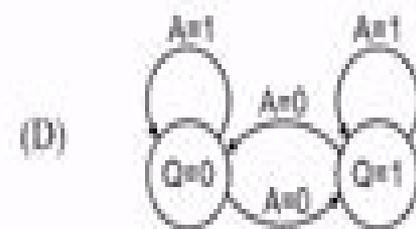
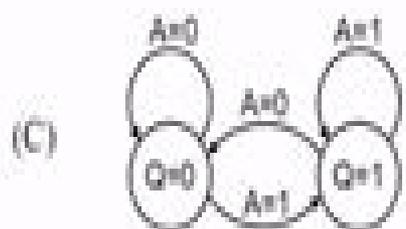
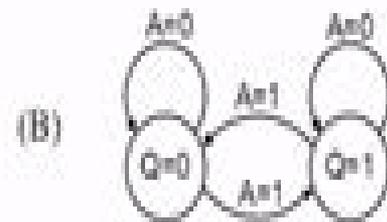
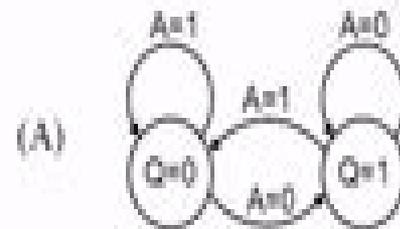
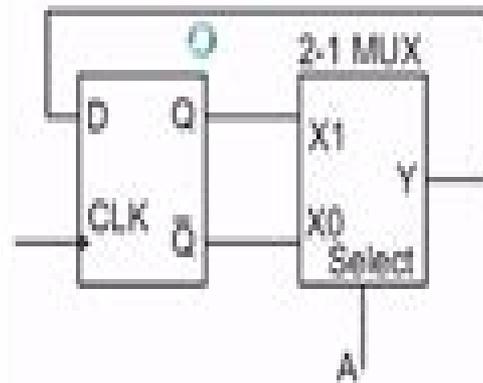


Q.43 The state transition diagram for the logic circuit shown is



Answer: **D**

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