

POKHARA UNIVERSITY

Level: Bachelor  
Programme: BCA  
Course: Digital Logic System (New)

Semester: Fall

Year : 2023  
Full Marks: 100  
Pass Marks: 45  
Time : 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

1. a) Perform the following conversions: 8
  - i.  $(E1B.AC5)_{16} = (?)_8$
  - ii.  $(237)_8 = (?)_2$
  - iii.  $(100110101)_{gray} = (?)_2$
  - iv.  $(1110)_2 = (?)_{10}$
- b) Show NAND and NOR gates as universal gates. 7
2. a) What is the dual of a function? Explain involution law. 7
- b) Simplify the following Boolean function with don't care condition. 8

$F(A, B, C, D) = \sum m(0, 1, 3, 4, 5, 6, 7, 11)$   
 $d(A, B, C, D) = \sum m(8, 10, 12, 15)$

Draw the logic circuit using NOR gates only.
3. a) Implement a circuit full adder. 8
- b) Convert Excess-3 to BCD code and use K-map for circuit implementation. 7
4. a) What is JK flipflop? Explain its operation with the help of logic circuit and truth table. 8
- b) Explain state diagram and state table. Explain it with example. 7
5. a) Explain PISO shift register. 8
- b) Design a 3-bit synchronous down ripple counter. 7
6. a) What is status register? Explain about RAM. 8
- b) Draw the circuit and explain 4-bit ALU. 7
7. Write short notes on: (Any two) 2×5
  - a) Gray Code
  - b) Postulates of boolean algebra
  - c) Maxterm vs Minterm

# First Sem Digital Logic Fundamental Question Paper

**BM King**



**First Sem Digital Logic Fundamental Question Paper:**

## Embracing the Beat of Appearance: An Psychological Symphony within **First Sem Digital Logic Fundamental Question Paper**

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