

- II. 5, 8, 9, 12, 10, 15, 25  
 III. 2, 7, 10, 8, 14, 16, 20  
 IV. 4, 6, 7, 9, 18, 20, 25  
 A. I and IV only  
 B. II and III only  
 C. II and IV only  
 D. II only

Answer III A

Solution III

An In order traversal of a Binary Search Tree must be in increasing order.

In-order Traversal the left sub tree by recursively calling the in-order function. Display the data part of the root (or current node). Traverse the right sub tree by recursively calling the in-order function. In a search tree, in-order traversal retrieves data in sorted order

8. Which one of the following is TRUE at any valid state in shift-reduce parsing?

- A. Viable prefixes appear only at the bottom of the stack and not inside  
 B. Viable prefixes appear only at the top of the stack and not inside  
 C. The stack contains only a set of viable prefixes  
 D. The stack never contains viable prefixes

Answer III C

Solution III

The prefixes of right sentential forms that can appear on the stack of a shift-reduce parser are called viable prefixes. By definition, a viable prefix is a prefix of a right sentential form that does not continue past the right end of the rightmost handle of that sentential form.

9. Which one of the following is NOT equivalent to  $p \leftrightarrow q$ ?

- A.  $(\neg p \vee q) \wedge (p \vee \neg q)$   
 B.  $(\neg p \vee q) \wedge (q \rightarrow p)$   
 C.  $(\neg p \wedge q) \vee (p \wedge \neg q)$   
 D.  $(\neg p \wedge \neg q) \vee (p \wedge q)$

Answer III C

Solution III

$p \leftrightarrow q$  means both  $p \rightarrow q$  and  $q \rightarrow p$ .  $p \leftrightarrow q$  is equivalent to  $(p \vee q) \wedge (\neg p \wedge \neg q)$  and B are fine.

D is a different way of writing  $A$ .  $p \leftrightarrow q$

$(p \rightarrow q) \wedge (q \rightarrow p) \equiv (p \vee q) \wedge (\neg p \wedge \neg q) \equiv (p \vee q) \wedge (\neg(p \wedge q)) \equiv (p \vee q) \wedge (\neg p \wedge \neg q) \vee (p \wedge q) \vee (\neg p \wedge \neg q)$  So, answer C

10. For a set A, the power set of A is denoted by  $2^A$ . If  $A = \{5, \{6\}, \{7\}\}$ , which of the following options are TRUE?

- I.  $\emptyset \in 2^A$   
 II.  $\emptyset \subseteq 2^A$   
 III.  $\{5, \{6\}\} \in 2^A$   
 IV.  $\{5, \{6\}\} \subseteq 2^A$   
 A. I and III only  
 B. II and III only  
 C. I, II and III only  
 D. I, II and IV only

Answer III C

Solution III

$A = \{5, \{6\}, \{7\}\}$

Below is Power set of A?

- $\{\emptyset, 5, \{6\}, \{7\}, \{5, \{6\}\}, \{5, \{7\}\}, \{6, \{7\}\}, \{5, \{6, \{7\}\}\}$

- $\{5, \{6\}\}, \{5, \{7\}\}, \{6, \{7\}\}, \{5, \{6, \{7\}\}\}$

I am true, as  $\emptyset$  belongs to Power set. II is true, as an empty set is a subset of every set. III is true as  $\{5, \{6\}\}$  belongs to Power set. IV is false,  $\{5, \{6\}\}$  is not a subset, but  $\{\{5, \{6\}\}\}$  is.

11. Consider a 4-bit Johnson counter with an initial value of 0000. The counting sequence of this counter is

- A. 0, 1, 3, 7, 15, 14, 12, 8, 0  
 B. 0, 1, 3, 5, 7, 9, 11, 13, 15, 0  
 C. 0, 2, 4, 6, 8, 10, 12, 14, 0  
 D. 0, 8, 12, 14, 15, 7, 3, 1, 0

Answer III D

Solution III

The four bit Johnson's counter connects the complement of the output of the last shift register to the input of the first register with shift distance = 1 i.e. 1 bit will shift/cycle. It will work as follows: 0000 //Last 0 complemented and fed as input to first register 1000 1100 1110 1111 //Last 1 complemented and fed as input to first register 0111 0011 0000

12. For computers based on three-address instruction formats, each address field can be used to specify which of the following:

- (S1) A memory operand  
 (S2) A processor register  
 (S3) An implied accumulator register  
 A. Either S1 or S2  
 B. Either S2 or S3  
 C. Only S2 and S3  
 D. All of S1, S2 and S3

Answer III A

Solution III

In Three address instruction format, each operand specifies either a memory address or a register.

13. Suppose two hosts use a TCP connection to transfer a large file. Which of the following statements is/are FALSE with respect to the TCP connection?

- I. If the sequence number of a segment is  $m$ , then the sequence number of the subsequent segment is always  $m+1$ .  
 II. If the estimated round trip time at any given point of time is  $t$  sec, the value of the retransmission timeout is always set to greater than or equal to  $t$  sec.  
 III. The size of the advertised window never changes during the course of the TCP connection.  
 IV. The number of unacknowledged bytes at the sender is always less than or equal to the advertised window.  
 A. III only  
 B. I and III only  
 C. I and IV only  
 D. II and IV only

Answer III B

Solution III

TCP sequence number of a segment is the byte number of the first byte in the segment. For example, if the segment contains 500 bytes which are from 1000 to

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