

MicroBlaze

Application Preset

Debug (JTAG)

Tightly Coupled
Local Memory

32-bit Processor Core

Instruction
Cache

Memory
Management
Unit

Data
Cache

SPI Controller

I2C Controller

UART

Interrupt Controller

Timer

OPIC

DDR Controller

Ethernet Controller

Programmable Logic

Microblaze User Guide

Ensheng Dong



Microblaze User Guide:

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