

LPC2148 PWM Programming

In this tutorial, we are going to discuss the PWM module of LPC2148. First, we will see how to configure the PWM registers to generate signals of required PWM, at the end, we will see how to use the Explore Embedded PWM library.

LPC2148 PWM Module

LPC2148 has 6 PWM output pins which can be used as 6-Single edged or 3-Double edged. There are seven-match registers to support these 6-PWM output signals. Below block diagram shows the PWM pins and the associated Match (Duty Cycle) registers.

PWM Channel	Port Pin	Pin Functions	Associated PINSEL Register	Corresponding Match Register
PWM_1	P0.0	0-GPIO, 1-TXD0, 2-PWM1	0,1 bits of PINSEL0	PWMMR1
PWM_2	P0.7	0-GPIO, 1-SSEL0, 2-PWM2, 3-EINT2	14,15 bits of PINSEL0	PWMMR2
PWM_3	P0.1	0-GPIO, 1-RXD0, 2-PWM3, 3-EINT0	2,3 bits of PINSEL0	PWMMR3
PWM_4	P0.8	0-GPIO, 1-TXD1, 2-PWM4	16,17 bits of PINSEL0	PWMMR4
PWM_5	P0.21	0-GPIO, 1-PWM5, 2-CAP1.3,	10,11 bits of PINSEL1	PWMMR5
PWM_6	P0.9	0-GPIO, 1-RXD1, 2-PWM6, 3-EINT3	18,19 bits of PINSEL0	PWMMR6

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M Woodhall



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Table of Contents Lpc2148 Programming Guide

1. Understanding the eBook Lpc2148 Programming Guide
 - The Rise of Digital Reading Lpc2148 Programming Guide
 - Advantages of eBooks Over Traditional Books
2. Identifying Lpc2148 Programming Guide
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Lpc2148 Programming Guide
 - User-Friendly Interface
4. Exploring eBook Recommendations from Lpc2148 Programming Guide
 - Personalized Recommendations
 - Lpc2148 Programming Guide User Reviews and Ratings

- Lpc2148 Programming Guide and Bestseller Lists
- 5. Accessing Lpc2148 Programming Guide Free and Paid eBooks
 - Lpc2148 Programming Guide Public Domain eBooks
 - Lpc2148 Programming Guide eBook Subscription Services
 - Lpc2148 Programming Guide Budget-Friendly Options
- 6. Navigating Lpc2148 Programming Guide eBook Formats
 - ePub, PDF, MOBI, and More
 - Lpc2148 Programming Guide Compatibility with Devices
 - Lpc2148 Programming Guide Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Lpc2148 Programming Guide
 - Highlighting and Note-Taking Lpc2148 Programming Guide
 - Interactive Elements Lpc2148 Programming Guide
- 8. Staying Engaged with Lpc2148 Programming Guide
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Lpc2148 Programming Guide
- 9. Balancing eBooks and Physical Books Lpc2148 Programming Guide
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Lpc2148 Programming Guide
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Lpc2148 Programming Guide
 - Setting Reading Goals Lpc2148 Programming Guide
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Lpc2148 Programming Guide
 - Fact-Checking eBook Content of Lpc2148 Programming Guide
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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